



FIG. 1


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graph TD
    60[HOST ISSUES DOWNLOAD REQUEST  
WITH NEW MEMORY AREA SPECIFIED] --> 64[MICROPROCESSOR RECEIVES  
NEW CODE/DATA AND STORES  
IN NEW MEMORY AREA]
    64 --> 66[MICROPROCESSOR VERIFIES  
NEW CODE/DATA]
    66 -- 68 --> 60
    66 --> 70[HOST ISSUES REQUEST TO  
MAKE NEW MEMORY AREA ACTIVE]
    70 --> 72[MICROPROCESSOR VERIFIES  
NEW MEMORY AREA]
    72 -- 74 --> 60
    72 --> 76[MICROPROCESSOR ALLOWS  
SELECTOR DEVICE TO  
DESIGNATE THE NEW MEMORY  
AREA AN ACTIVE MEMORY AREA]
    76 --> 78[SELECTOR DEVICE INACTIVATES  
MEMORY AREA UPON REQUEST]

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FIG. 3

